

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Havemann et al.

Examiner: TBD

Serial No: TBD

Art Unit: TBD

Filed: 11/01/01

Docket No.: TI-27506

For: INTEGRATED CIRCUIT HAVING A DOPED POROUS DIELECTRIC AND
METHOD OF MANUFACTURING THE SAME

2-19-02
J1046 U.S. PTO
10/001472
11/01/01

INFORMATION DISCLOSURE STATEMENT

November 1, 2001

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

Please make the references listed on the enclosed PTO-1449 of record under 37 C.F.R. 1.56, 1.97, and 1.98 in the patent application identified above. Copies of the listed references are enclosed.

Respectfully submitted,

Jacqueline J. Garner
Attorney for Applicants
Reg. No. 36,144

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972)271-1176 or (972) 917-5643
(972)917-4418 Fax